

ABSTRACT

A display monitor has at least a stand-alone operational mode, and a further operational mode as a further peripheral to, e.g., a PC. The monitor has an input for connecting to a first peripheral device. The monitor also has circuitry for enabling data communication with the first peripheral device for user control of a functionality of the display monitor in the stand-alone mode, and for data communication between the first peripheral and the PC via the monitor when the monitor is in the further operational mode and connected to the PC and to the first peripheral.

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